

Port IO	PC	PC/XT	PCJr (Tandy 1000)	AT	PS/2	PS/1	Other
0x61	0x60-0x63 PPI 8255A-5 (0x61: bit 4- Enable READ/WRITE MEMORY; bit 5 - ENABLE I/O CH CK; bit 6 - HOLD KBD CLK LOW)	0x60-0x63 PPI 8255A-5 (0x61: bit 4- Enable RAM Parity Check; bit 5 - Enable I/O Channel Check; bit 6 - Hold Keyboard Clock Low)	1. 0x60-0x67 PPI 8255-5 (0x61: Port bits PB5 and PB6, of the 8255, control which source is selected) 2. 0x60-0x63 PIO (0x61: bit 5 - Sound Control 0; bit 6 - Sound Control 1) 3. 0x60-0x6F PIO Function (0x61: bit 4 - 1=Disable Sound Chip (Sound Control 2); bit 5 - Not Used; bit 6 - 0=HOLOCK (If IBM PC Keyboard Mode))	0x60-0x6F 8042 (Keyboard)	0x61 System Control Port B (bits 6-4 - Reserved)	0x61-0x6F (odd bytes) System Control Port B (0x61: bits 7-4 - Reserved)	
0xC0	0xC? Reserved	0xC? Reserved	1. 0xC0-0xC7 Sound SN76496N 2. 0xC0-0xC7 Sound Generator 3. 0xC0-0xC7 Sound Generator (0xC0-0xC3 Sound SN76496; 0xC4-0xC7 DAC Functions) 4. 0xC0 Sound Generator	0xC0-0xDF DMA controller 2, 8237A-5	0xC0-0xDF DMA Controller (4-7)	0xC0-0xDE (even bytes) DMA Controller, Channels 4-7	
0xE0	0xE? Reserved	0xE? Reserved	2. 0xE0-0x1FF Reserved 3. 0xE0-0xFF Numerical Coprocessor 4. 0xE0 Math Coprocessor	I/O Addresses, hex 00 to FF, are reserved for the system board I/O	0xE0-0xE1 Memory Encoding Registers		0xE0-0xE7 Microchannel
0x1C0			2. 0xE0-0x1FF Reserved 3. 0x100-0x1FF Reserved	Hex 100 to 3FF are available on the I/O channel			
0x1E0			2. 0xE0-0x1FF Reserved 3. 0x100-0x1FF Reserved 4. 0x1E0 Tandy Sound	Hex 100 to 3FF are available on the I/O channel			
0x205	0x200-0x20F Game I/O Adapter	0x200-0x20F Game Control	1. 0x200-0x207 Joystick 2. 0x200-0x20F Joystick I/F 3. 0x200-0x20F Joystick Interface	0x200-0x207 Game I/O		0x?200-0x?207 Audio Card (0x205 Sound Generator)	0x200-0x2FF Sunshine uPW48, programmer for EPROM version CPU's 8748/8749 (4 bit DIP switch installable in the range 20x-2Fx)
0x2C0			2. 0x210-0x31F Reserved 3. 0x210-0x2F7 Reserved	0x2B0-0x2DF Alternate Enhanced Graphics Adapter			0x2C0-0x2C? AST-clock
0x2E0			2. 0x210-0x31F Reserved 3. 0x210-0x2F7 Reserved	0x2E1 GPIB (Adapter 0)			0x2E0-0x2EF data aquisition (AT)?
0xFFEB			2. 0x400-0xFFFF Not Usable 3. 0xFFE8-0xFFEF System Programming Options (Non IBM compatible) (0xFFEB: bit 2 - 0=Disable Sound Chip, 1=Enable Sound Chip)				
Source	Technical Reference 6025008. The Personal Computer. Hardware Library, First Edition (August 1981), pp. 2-23, 2-24)	Technical Reference 1502237. The Personal Computer XT. Hardware Library, Revised Edition (April 1983), pp. 1-8, 1-10	1. Technical Reference 6322963. The Personal Computer PCJr. Hardware Library, First Edition Revised (November 1983), pp. 2-29, 2-88, 2-89 2. Technical Reference Manual Tandy 1000 25-1504 (1984), pp. 58, 65 3. Technical Reference Manual Tandy 1000 SL 25-1516 (1988), pp. 24, 36, 39-42, 56 4. 1000's Tech Notes & Jumper Manual. Volume 1 (1993), p. 70	Technical Reference 6183355. The Personal Computer. Hardware Reference Library, Revised Edition (March 1986), p. 1-28	Technical Reference 1502237. Personal System/2 Model 80, First Edition (May 1988), pp. 1-6, 3-49	Technical Reference for IBM PS/1 Computer, First Edition (April 1990), p. 1-7, 3-165, A-13	XT, AT and PS/2 I/O port addresses (bochs.sourceforge.io/techspec/PORTS.LST)