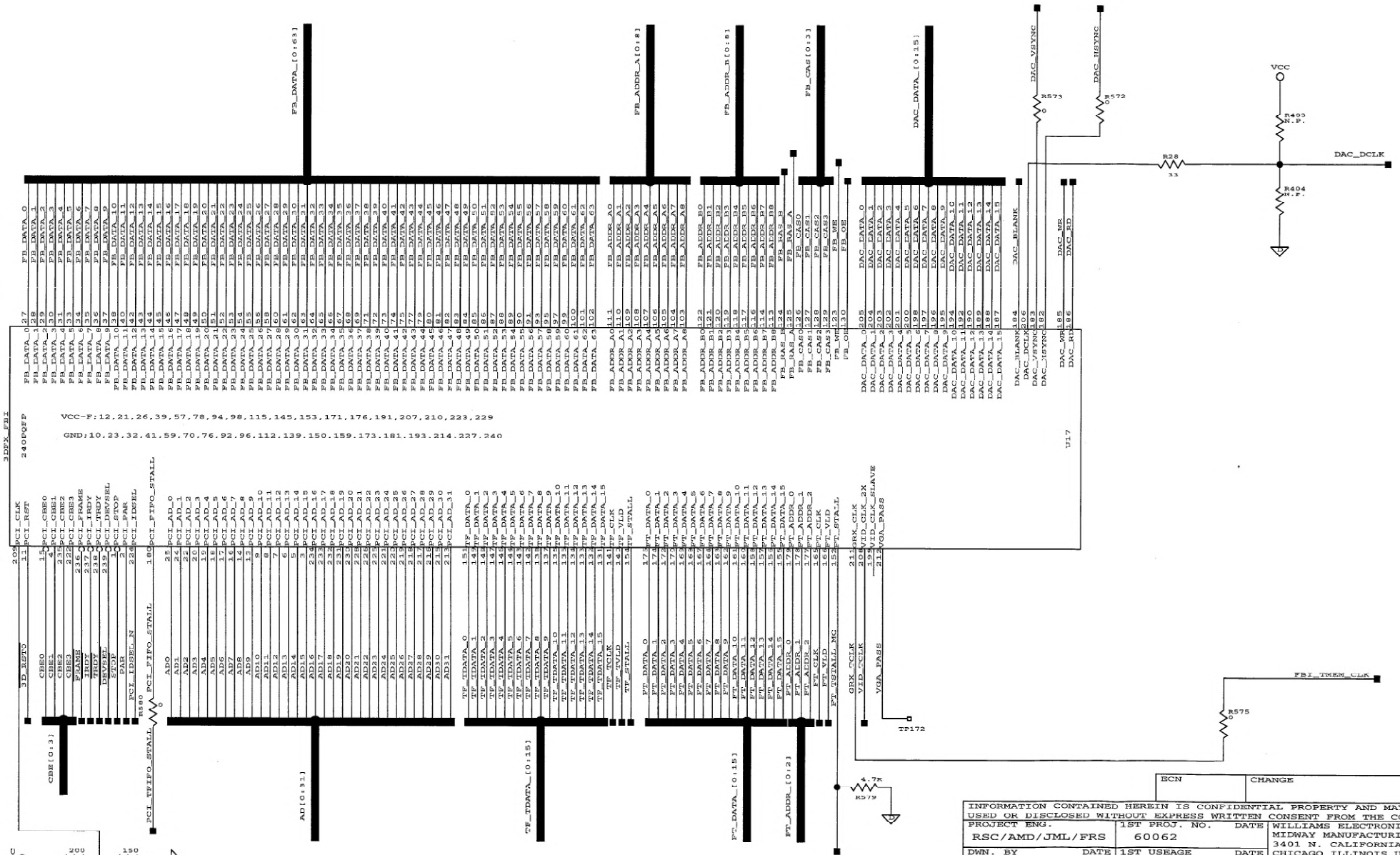


## D

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PROJECT ENG.	1ST PROJ. NO.	DATE	WILLIAMS ELECTRONICS GAMES	
RSC/AMD/JML/FRS	60062		MIDWAY MANUFACTURING CO	
DWN. BY	DATE	1ST USAGE	3401 N. CALIFORNIA AVENUE CHICAGO ILLINOIS USA	
RSC/AMD/JML/FRS			TITLE A 21464 SEATTLE	
CHECKED	DATE	CURRENT PROJ.	DATE	
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APPROVED	DATE	SHEET	32 OF 46	PART NO.
				5770-15206-00
				REV 00

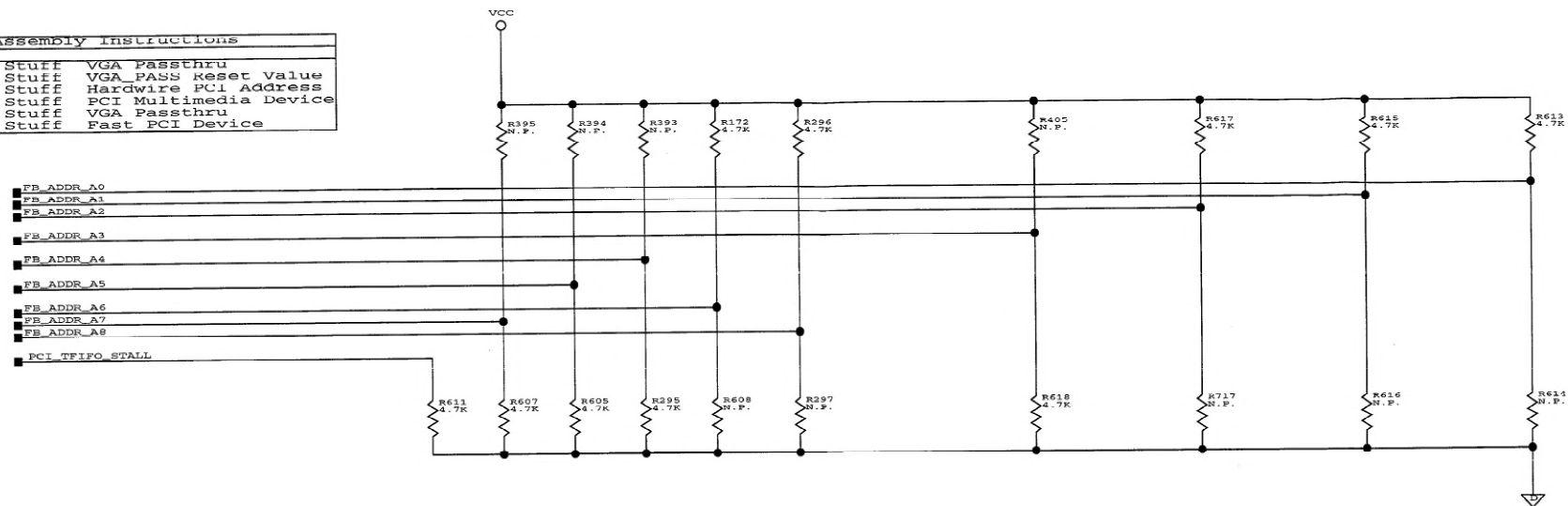
## FRAME BUFFER INTERFACE (FBI)



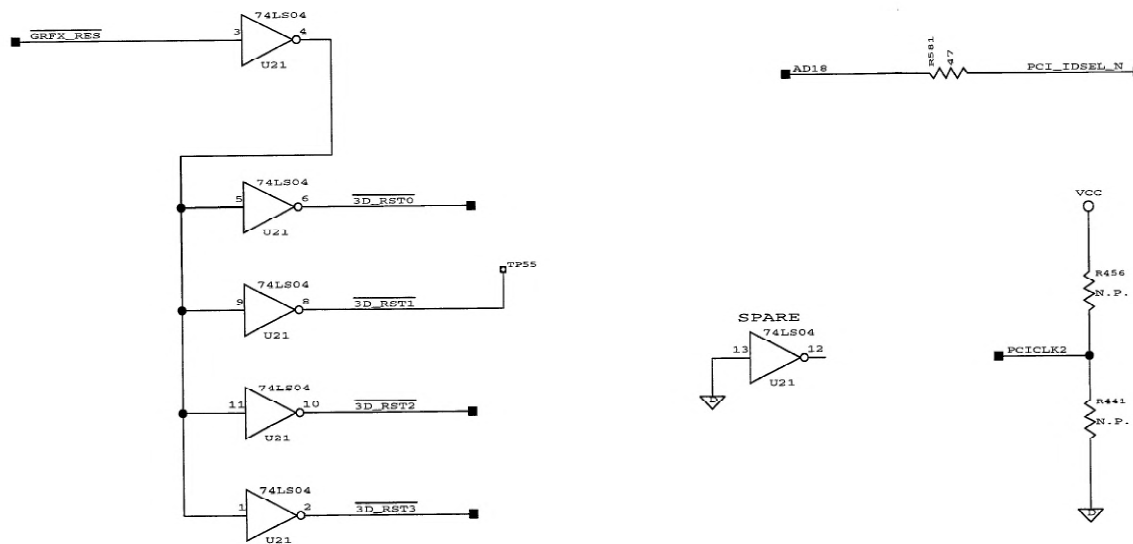
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PROJECT ENG.		1ST PROJ. NO.	DATE	
RSC/AMD/JML/FRS		60062	WILLIAMS ELECTRONICS GAMES MIDWAY MANUFACTURING CO. 3401 N. CALIFORNIA AVENUE CHICAGO ILLINOIS USA	
DWN. BY		DATE	1ST USAGE	DATE
RSC/AMD/JML/FRS				
CHECKED		DATE	CURRENT PROJ.	DATE
APPROVED			TITLE	
DATE		SHEET 33 OF 46	A-214G4 SEATTLE	
			9-18-1996_14:15	
APPROVED			DATE NO.	
DATE			5770-15206-00	
			REV. 00	

# FBI PULL-UPS AND POWER ON RESISTORS

Power Up Assembly Instructions			
A3	R618	Stuff	VGA Passthru
A4	R393	Stuff	VGA_PASS Reset Value
A5	R605	Stuff	Hardware PCI Address
A6	R606	Stuff	PCI Multimedia Device
A7	R607	Stuff	VGA Passthru
A8	R296	Stuff	Fast PCI Device



## VIDEO SUBSYSTEM RESETS



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RSC/AMD/JML/FRS	60062	DATE	MILWAY MANUFACTURING CO.	
DWN. BY	DATE	1ST USAGE	DATE	3401 N. CALIFORNIA AVENUE
RSC/AMD/JML/FRS	DATE	CURRENT PROJ.	DATE	CHICAGO ILLINOIS USA
CHECKED	DATE	TITLE		
		A-21464		
		SEATTLE		
		10-2-1996_11:38		
APPROVED	DATE	SHEET	34 OF 46	PART NO.
				5770-15206-00
				REV.
				00

# FRAME BUFFER TERMINATIONS

FB\_TDATA\_[0:63]

FB\_DATA\_[0:63]

FB\_DATA\_0 R623 100 FB\_TDATA\_0  
FB\_DATA\_1 R622 100 FB\_TDATA\_1  
FB\_DATA\_2 R621 100 FB\_TDATA\_2  
FB\_DATA\_3 R624 100 FB\_TDATA\_3

FB\_DATA\_4 R627 100 FB\_TDATA\_4  
FB\_DATA\_5 R626 100 FB\_TDATA\_5  
FB\_DATA\_6 R625 100 FB\_TDATA\_6  
FB\_DATA\_7 R628 100 FB\_TDATA\_7

FB\_DATA\_8 R631 100 FB\_TDATA\_8  
FB\_DATA\_9 R630 100 FB\_TDATA\_9  
FB\_DATA\_10 R629 100 FB\_TDATA\_10  
FB\_DATA\_11 R632 100 FB\_TDATA\_11

FB\_DATA\_12 R635 100 FB\_TDATA\_12  
FB\_DATA\_13 R634 100 FB\_TDATA\_13  
FB\_DATA\_14 R633 100 FB\_TDATA\_14  
FB\_DATA\_15 R636 100 FB\_TDATA\_15

FB\_DATA\_16 R639 100 FB\_TDATA\_16  
FB\_DATA\_17 R638 100 FB\_TDATA\_17  
FB\_DATA\_18 R637 100 FB\_TDATA\_18  
FB\_DATA\_19 R640 100 FB\_TDATA\_19

FB\_DATA\_20 R643 100 FB\_TDATA\_20  
FB\_DATA\_21 R642 100 FB\_TDATA\_21  
FB\_DATA\_22 R641 100 FB\_TDATA\_22  
FB\_DATA\_23 R644 100 FB\_TDATA\_23

FB\_DATA\_24 R647 100 FB\_TDATA\_24  
FB\_DATA\_25 R646 100 FB\_TDATA\_25  
FB\_DATA\_26 R645 100 FB\_TDATA\_26  
FB\_DATA\_27 R648 100 FB\_TDATA\_27

FB\_DATA\_28 R651 100 FB\_TDATA\_28  
FB\_DATA\_29 R650 100 FB\_TDATA\_29  
FB\_DATA\_30 R649 100 FB\_TDATA\_30  
FB\_DATA\_31 R652 100 FB\_TDATA\_31

FB\_DATA\_32 R655 100 FB\_TDATA\_32  
FB\_DATA\_33 R654 100 FB\_TDATA\_33  
FB\_DATA\_34 R653 100 FB\_TDATA\_34  
FB\_DATA\_35 R656 100 FB\_TDATA\_35

FB\_DATA\_36 R659 100 FB\_TDATA\_36  
FB\_DATA\_37 R658 100 FB\_TDATA\_37  
FB\_DATA\_38 R657 100 FB\_TDATA\_38  
FB\_DATA\_39 R660 100 FB\_TDATA\_39

FB\_DATA\_40 R663 100 FB\_TDATA\_40  
FB\_DATA\_41 R662 100 FB\_TDATA\_41  
FB\_DATA\_42 R661 100 FB\_TDATA\_42  
FB\_DATA\_43 R664 100 FB\_TDATA\_43

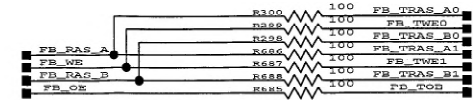
FB\_DATA\_44 R667 100 FB\_TDATA\_44  
FB\_DATA\_45 R666 100 FB\_TDATA\_45  
FB\_DATA\_46 R665 100 FB\_TDATA\_46  
FB\_DATA\_47 R668 100 FB\_TDATA\_47

FB\_DATA\_48 R670 100 FB\_TDATA\_48  
FB\_DATA\_49 R671 100 FB\_TDATA\_49  
FB\_DATA\_50 R672 100 FB\_TDATA\_50  
FB\_DATA\_51 R669 100 FB\_TDATA\_51

FB\_DATA\_52 R674 100 FB\_TDATA\_52  
FB\_DATA\_53 R673 100 FB\_TDATA\_53  
FB\_DATA\_54 R676 100 FB\_TDATA\_54  
FB\_DATA\_55 R675 100 FB\_TDATA\_55

FB\_DATA\_56 R678 100 FB\_TDATA\_56  
FB\_DATA\_57 R677 100 FB\_TDATA\_57  
FB\_DATA\_58 R670 100 FB\_TDATA\_58  
FB\_DATA\_59 R679 100 FB\_TDATA\_59

FB\_DATA\_60 R682 100 FB\_TDATA\_60  
FB\_DATA\_61 R681 100 FB\_TDATA\_61  
FB\_DATA\_62 R684 100 FB\_TDATA\_62  
FB\_DATA\_63 R683 100 FB\_TDATA\_63



FB\_CAS0 R690 100 FB\_TCAS0  
FB\_CAS1 R691 100 FB\_TCAS1  
FB\_CAS2 R692 100 FB\_TCAS2  
FB\_CAS3 R693 100 FB\_TCAS3

ALL Terminations should be 100 ohms  
R619, R620, R693 708 Should Be Stuffed with 47-ohm  
When Using 500 0003-02 (.5u)  
When Using 500-0003-03 (.5u)

FB\_ADDR\_A[0:8]

FB\_ADDR\_A0 R695 47 FB\_TADDR\_A0  
FB\_ADDR\_A1 R696 47 FB\_TADDR\_A1  
FB\_ADDR\_A2 R697 47 FB\_TADDR\_A2  
FB\_ADDR\_A3 R700 47 FB\_TADDR\_A3

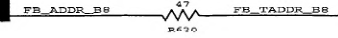
FB\_ADDR\_A4 R695 47 FB\_TADDR\_A4  
FB\_ADDR\_A5 R694 47 FB\_TADDR\_A5  
FB\_ADDR\_A6 R693 47 FB\_TADDR\_A6  
FB\_ADDR\_A7 R692 47 FB\_TADDR\_A7



FB\_ADDR\_B[0:8]

FB\_ADDR\_B0 R707 47 FB\_TADDR\_B0  
FB\_ADDR\_B1 R706 47 FB\_TADDR\_B1  
FB\_ADDR\_B2 R705 47 FB\_TADDR\_B2  
FB\_ADDR\_B3 R708 47 FB\_TADDR\_B3

FB\_ADDR\_B4 R702 47 FB\_TADDR\_B4  
FB\_ADDR\_B5 R701 47 FB\_TADDR\_B5  
FB\_ADDR\_B6 R704 47 FB\_TADDR\_B6  
FB\_ADDR\_B7 R703 47 FB\_TADDR\_B7

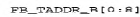


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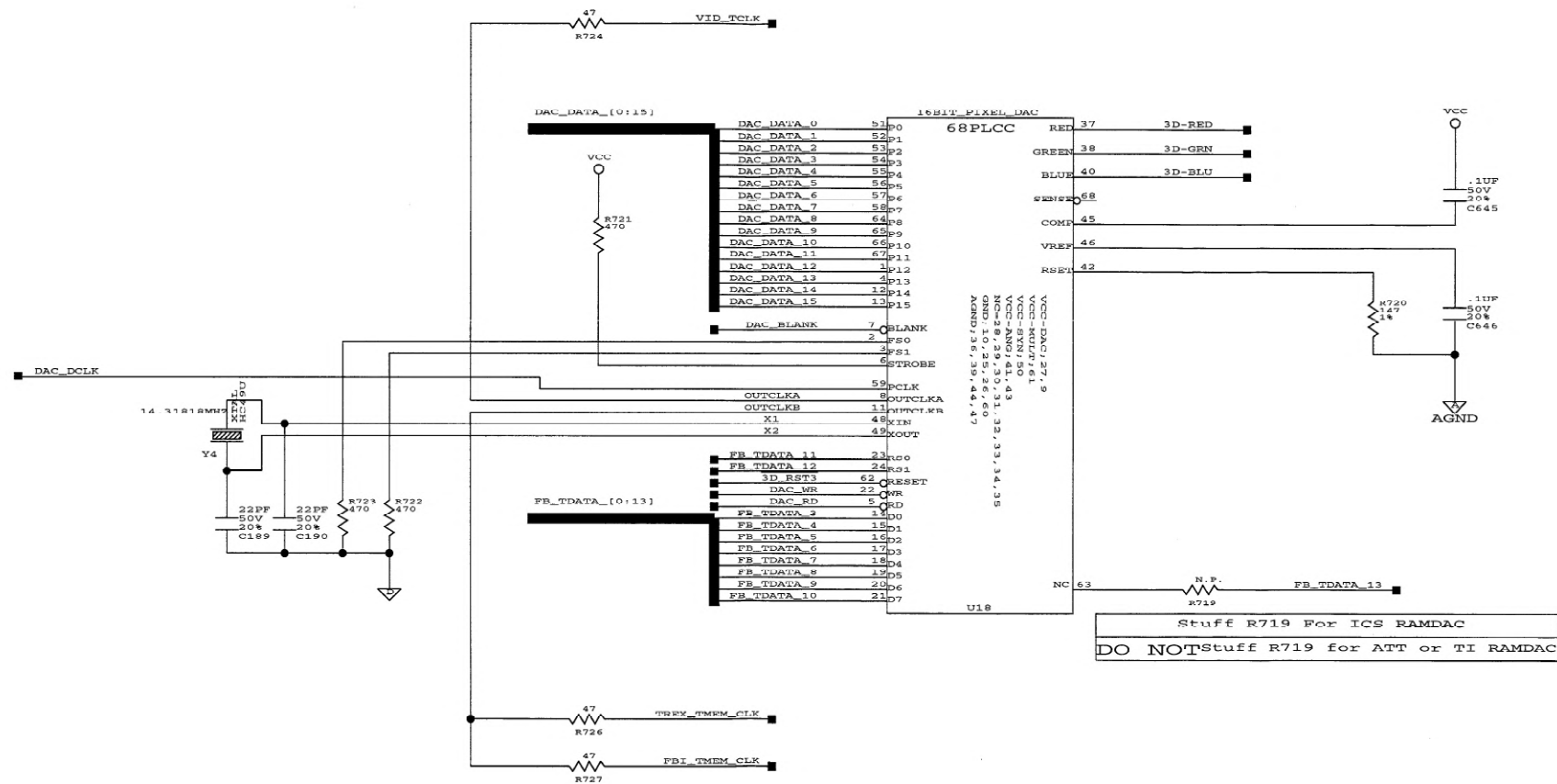
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PROJECT ENG.	1ST PROJ. NO.	DATE	WILLIAMS ELECTRONICS GAMES
RSC/AMD/JML/FRS	60062		MILWAY MANUFACTURING CO.
DWN. BY	DATE	1ST USAGE	DATE
RSC/AMD/JML/FRS			3401 N. CALIFORNIA AVENUE
CHECKED	DATE	CURRENT PROJ.	DATE
			CHICAGO ILLINOIS USA
APPROVED	DATE	SHEET	35 OF 46
		PART NO.	5770-15206-00
		REV.	00

TITLE A-21464  
SEATTLE  
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# CLOCK GENERATOR RAMDAC



Stuff R719 For ICS RAMDAC  
DO NOT stuff R719 for ATT or TI RAMDAC

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PROJECT ENG.	1ST PROJ. NO.	DATE	WILLIAMS ELECTRONICS GAMES	
RSC/AMD/JML/FRS	60062		MIDWAY MANUFACTURING CO.	
DWN. BY	DATE	1ST USAGE	DATE	3401 N. CALIFORNIA AVENUE
RSC/AMD/.TMT./FRS				CHICAGO ILLINOIS USA
CHECKED	DATE	CURRENT PROJ.	DATE	TITLE
				A-31464
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				9-18-1996_13:15
APPROVED	DATE	SHEET	37 OF 46	PART NO.
				5770-15206-00
				REV. 00