



PI74AVC+16244

2.5V 16-Bit Buffer Driver with 3-State Outputs

Product Features

- PI74AVC+16244 is designed for low-voltage operation, $V_{CC} = 1.65V$ to $3.6V$
- True $\pm 24mA$ Balanced Drive @ $3.3V$
- Compatible with Philips and T.I. AVC Logic family
- I_{OFF} supports partial power-down operation
- $3.6V$ I/O Tolerant inputs and outputs
- All outputs contain a patented DDC (Dynamic Drive Control) circuit that reduces noise without degrading propagation delay
- Industrial operation: $-40^{\circ}C$ to $+85^{\circ}C$
- Available Packages:
 - 48-pin 240-mil wide plastic TSSOP
 - 48-pin 173-mil wide plastic TVSOP

Product Description

Pericom Semiconductor's PI74AVC+ series of logic circuits are produced using the Company's advanced submicron CMOS technology, achieving industry leading speed.

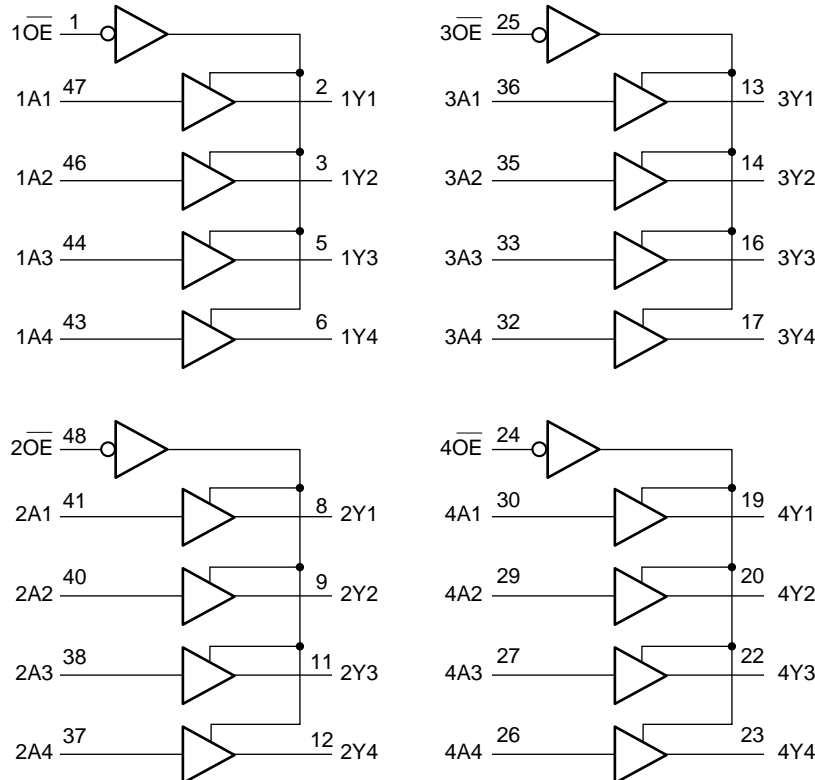
PI74AVC+16244 is a noninverting 16-bit buffer/driver designed for low-voltage $1.65V$ to $3.6V$ V_{CC} operation.

The buffer/driver is designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters.

The device can be used as four 4-bit buffers, two 8-bit buffers, or one 16-bit buffer. It provides inverting outputs and symmetrical active-low output-enable (OE) inputs.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pullup resistor in which the minimum value is determined by the current-sinking capability of the driver.

Logic Block Diagram



Maximum Ratings

(Above which the useful life may be impaired.

For user guidelines, not tested.)

Supply voltage range, V_{CC}	-0.5V to +4.6V
Input voltage range, V_I	-0.5V to +4.6V
Voltage range applied to any output in the high-impedance or power-off state, $V_O^{(1)}$	-0.5V to +4.6V
Voltage range applied to any output in the high or low state, $V_O^{(1,2)}$	-0.5V to $V_{CC}+0.5V$
Input clamp current, I_{IK} ($V_I < 0$)	-50mA
Output clamp current, I_{OK} ($V_O < 0$)	-50mA
Continuous output current, I_O	$\pm 50mA$
Continuous current through each V_{CC} or GND	$\pm 100mA$
Package thermal impedance, $\theta_{JA}^{(3)}$: package A	64°C/W
package K	48°C/W
Storage Temperature range, T_{stg}	-65°C to 150°C

Notes:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

1. Input & output negative-voltage ratings may be exceeded if the input and output current rating are observed.
2. Output positive-voltage rating may be exceeded up to 4.6V maximum if the output current rating is observed.
3. The package thermal impedance is calculated in accordance with JESD51.

Truth Table⁽¹⁾

Inputs		Outputs
\overline{nOE}	nAx	nYx
L	H	H
L	L	L
H	X	Z

Notes:

1. H = High Signal Level
L = Low Signal Level
X = Don't Care or Irrelevant
Z = High Impedance

Product Pin Description

Pin Name	Description
\overline{nOE}	3-State Output Enable Inputs (Active LOW)
nAx	Inputs
nYx	3-State Outputs
GND	Ground
V_{CC}	Power

Product Pin Configuration

1 \overline{OE}	1 \circ	48 \square	2 \overline{OE}
1Y1 \square	2	47 \square	1A1
1Y2 \square	3	46 \square	1A2
GND \square	4	45 \square	GND
1Y3 \square	5	44 \square	1A3
1Y4 \square	6	43 \square	1A4
V_{CC} \square	7	42 \square	V_{CC}
2Y1 \square	8	41 \square	2A1
2Y2 \square	9	40 \square	2A2
GND \square	10	39 \square	GND
2Y3 \square	11	38 \square	2A3
2Y4 \square	12	37 \square	2A4
3Y1 \square	13	36 \square	3A1
3Y2 \square	14	35 \square	3A2
GND \square	15	34 \square	GND
3Y3 \square	16	33 \square	3A3
3Y4 \square	17	32 \square	3A4
V_{CC} \square	18	31 \square	V_{CC}
4Y1 \square	19	30 \square	4A1
4Y2 \square	20	29 \square	4A2
GND \square	21	28 \square	GND
4Y3 \square	22	27 \square	4A3
4Y4 \square	23	26 \square	4A4
4 \overline{OE} \square	24	25 \square	3 \overline{OE}

Recommended Operating Conditions⁽¹⁾

		Min.	Max.	Units
V_{CC} Supply Voltage	Operating	1.4	3.6	V
	Data retention only	1.2		
V_{IH} High-level Input Voltage	$V_{CC} = 1.2V$	V_{CC}		
	$V_{CC} = 1.4V$ to $1.6V$	$0.65 \times V_{CC}$		
	$V_{CC} = 1.65V$ to $1.95V$	$0.65 \times V_{CC}$		
	$V_{CC} = 2.3V$ to $2.7V$	1.7		
	$V_{CC} = 3V$ to $3.6V$	2		
V_{IL} Low-level Input Voltage	$V_{CC} = 1.2V$		GND	
	$V_{CC} = 1.4V$ to $1.6V$		$0.35 \times V_{CC}$	
	$V_{CC} = 1.65V$ to $1.95V$		$0.35 \times V_{CC}$	
	$V_{CC} = 2.3V$ to $2.7V$		0.7	
	$V_{CC} = 3V$ to $3.6V$		0.8	
V_I Input Voltage		0	3.6	
V_O Output Voltage	Active State	0	V_{CC}	
	3-State	0	3.6	
I_{OHS} High-level output current	$V_{CC} = 1.4V$ to $1.6V$		-4	mA
	$V_{CC} = 1.65V$ to $1.95V$		-6	
	$V_{CC} = 2.3V$ to $2.7V$		-12	
	$V_{CC} = 3V$ to $3.6V$		-24	
I_{OLS} Low-level output current	$V_{CC} = 1.4V$ to $1.6V$		4	
	$V_{CC} = 1.65V$ to $1.95V$		6	
	$V_{CC} = 2.3V$ to $2.7V$		12	
	$V_{CC} = 3V$ to $3.6V$		24	
$\Delta t \Delta v$ Input transition rise or fall rate	$V_{CC} = 1.4V$ to $3.6V$		5	ns/V
T_A Operating free-air temperature		-40	85	°C

Notes:

1. All unused inputs must be held at V_{CC} or GND to ensure proper device operation.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C} + 85^\circ\text{C}$)

Parameters		Test Conditions ⁽¹⁾	V _{CC}	Min.	Typ.	Max.	Units
V _{OH}		I _{OH} = −100μA	1.4V to 3.6V	V _{CC} −0.2V			V
		I _{OHS} = −4mA V _{IH} = 0.91V	1.4V	1.05			
		I _{OHS} = −6mA V _{IH} = 1.07V	1.65V	1.2			
		I _{OHS} = −12mA V _{IH} = 1.7V	2.3V	1.75			
		I _{OHS} = −24mA V _{IH} = 2V	3V	2.0			
V _{OL}		I _{OLS} = 100μA	1.4V to 3.6V			0.2	V
		I _{OLS} = 4mA V _{IL} = 0.49V	1.4V			0.4	
		I _{OLS} = 6mA V _{IL} = 0.57V	1.65V			0.45	
		I _{OLS} = 12mA V _{IL} = 0.7V	2.3V			0.55	
		I _{OLS} = 24mA V _{IL} = 0.8V	3V			0.8	
I _I		V _I = V _{CC} or GND	3.6V			±2.5	μA
I _{OFF}		V _I or V _O = 3.6V	0			±10	
I _{OZ}		V _O = V _{CC} or GND	3.6V			±10	
I _{CC}		V _I = V _{CC} or GND I _O = 0	3.6V			40	
C _I	Control Inputs	V _I = V _{CC} or GND	2.5V		3.5		pF
			3.3V		3.5		
	Data Inputs		2.5V		6		
			3.3V		6		
C _O	Outputs	V _O = V _{CC} or GND	2.5V		6.5		
			3.3V		6.5		

Note:

1. Typical values are measured at $T_A = 25^\circ\text{C}$.

Switching Characteristics

(Over recommended operating free-air temperature range, unless otherwise noted, see Figures 1 thru 4)

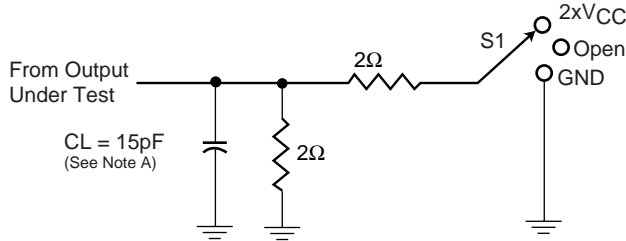
Parameters	From (Input)	To (Output)	V _{CC} = 1.2V	V _{CC} = 1.5V ± 0.1V		V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		Units
			Typ.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
t _{pd}	A	Y	3.1	0.6	3.3	0.7	2.9	0.6	1.9	0.5	1.7	ns
t _{en}	$\overline{\text{OE}}$	Y	7.6	1.4	8	1.3	6.8	0.9	4.0	0.7	3.5	
t _{dis}	$\overline{\text{OE}}$	Y	7.2	1.7	7.3	1.6	6.2	1.0	4.3	1.0	3.5	

Operating Characteristics, T_A = 25°C

Parameters		Test Conditions	V _{CC} = 1.8V ±0.15V	V _{CC} = 2.5V ±0.2V	V _{CC} = 3.3V ±0.3V	Units
			Typical	Typical	Typical	
C _{pd} Power Dissipation Capacitance	Outputs Enabled	C _L = 0pF, f = 10 MHz	23	27	33	pF
	Outputs Disabled		0.1	0.1	0.1	

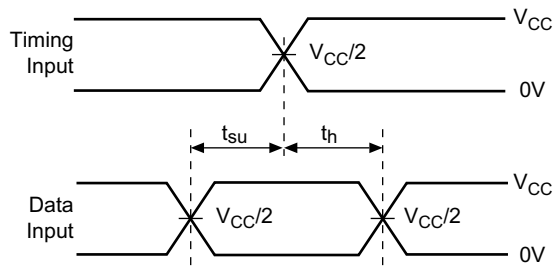
PARAMETER MEASUREMENT INFORMATION

$V_{CC} = 1.2V \text{ and } 1.5V \pm 0.1V$

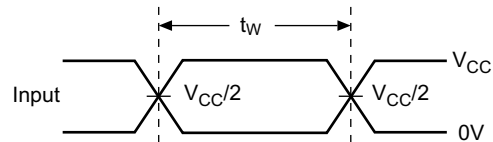


Load Circuit

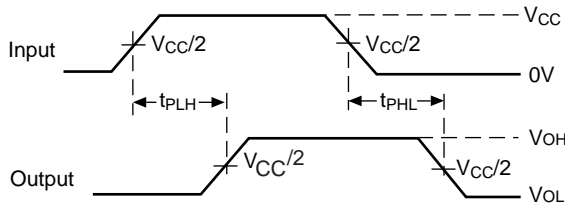
Test	S1
t_{pd} t_{PLZ}/t_{PZL} t_{PHZ}/t_{PZH}	Open $2 \times V_{CC}$ GND



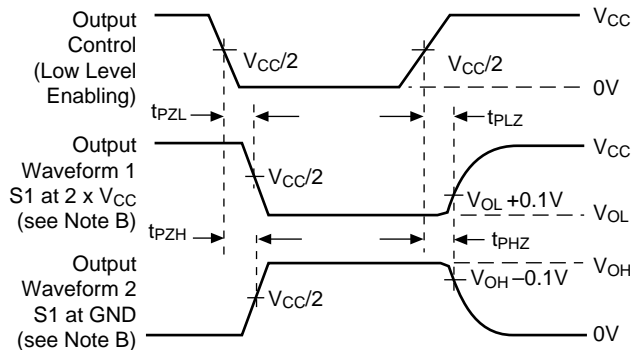
**Voltage Waveforms
Setup and Hold Times**



**Voltage Waveforms
Pulse Duration**



**Voltage Waveforms
Propagation Delay Times**



**Voltage Waveforms
Enable and Disable Times**

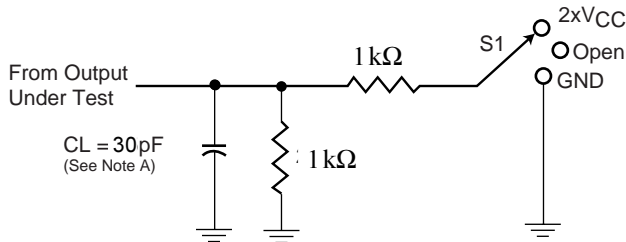
Figure 1. Load Circuit and Voltage Waveforms

Notes:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.0ns$, $t_F \leq 2.0ns$.
- The outputs are measured one at a time with one transition per measurement.
- t_{PLZ} and t_{PHZ} are the same as t_{dis}
- t_{PZL} and t_{PZH} are the same as t_{en}
- t_{PLH} and t_{PHL} are the same as t_{pd}

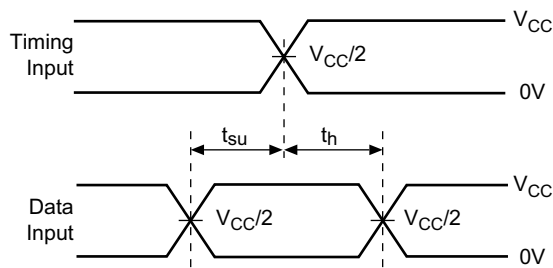
PARAMETER MEASUREMENT INFORMATION

$$V_{CC} = 1.8V \pm 0.15V$$

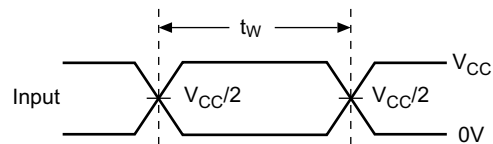


Load Circuit

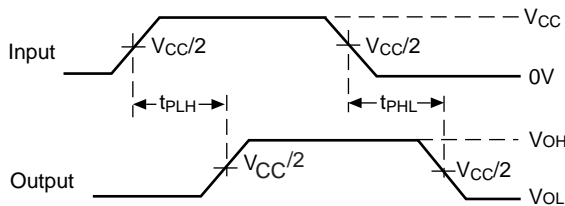
Test	S1
t_{pd} t_{PLZ}/t_{PZL} t_{PHZ}/t_{PZH}	Open 2 x V $_{CC}$ GND



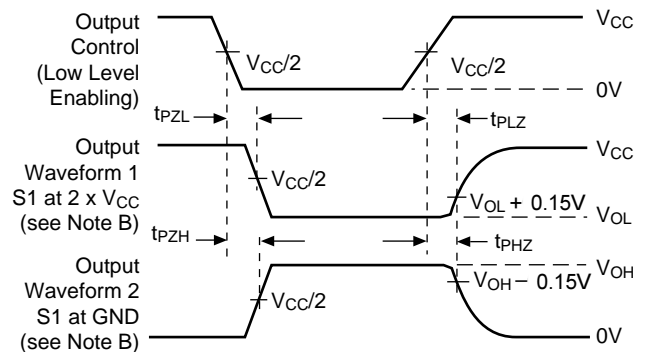
**Voltage Waveforms
Setup and Hold Times**



**Voltage Waveforms
Pulse Duration**



**Voltage Waveforms
Propagation Delay Times**



**Voltage Waveforms
Enable and Disable Times**

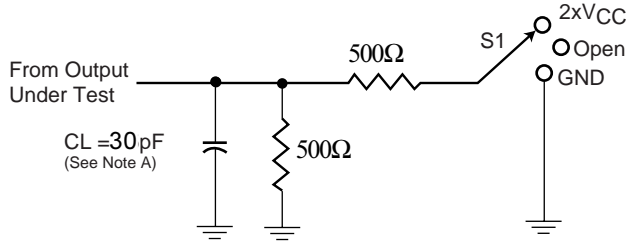
Figure2. Load Circuit and Voltage Waveforms

Notes:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.0ns$, $t_F \leq 2.0ns$.
- The outputs are measured one at a time with one transition per measurement.
- t_{PLZ} and t_{PHZ} are the same as t_{dis}
- t_{PZL} and t_{PZH} are the same as t_{en}
- t_{PLH} and t_{PHL} are the same as t_{pd}

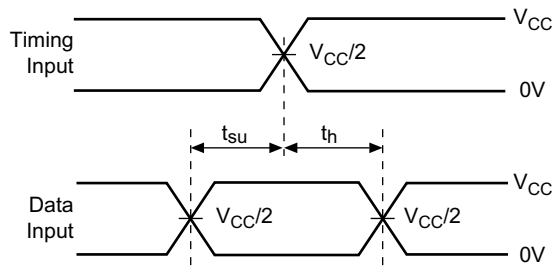
PARAMETER MEASUREMENT INFORMATION

$$V_{CC} = 2.5V \pm 0.2V$$

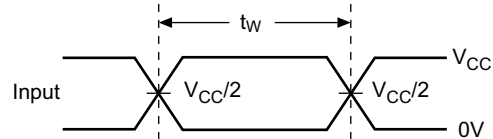


Load Circuit

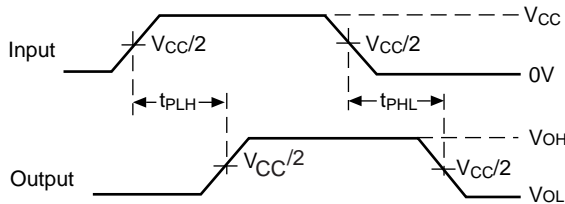
Test	S1
t_{pd} t_{PLZ}/t_{PZL} t_{PHZ}/t_{PZH}	Open 2 x V_{CC} GND



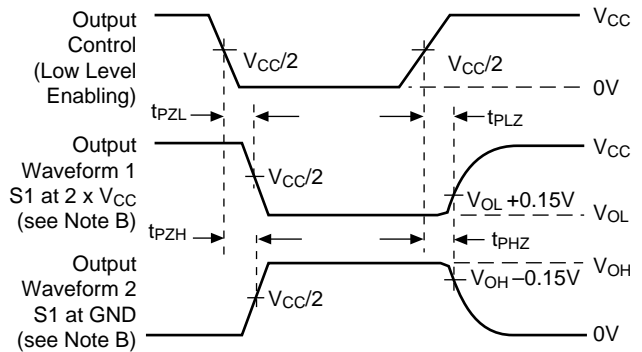
Voltage Waveforms
Setup and Hold Times



Voltage Waveforms
Pulse Duration



Voltage Waveforms
Propagation Delay Times



Voltage Waveforms
Enable and Disable Times

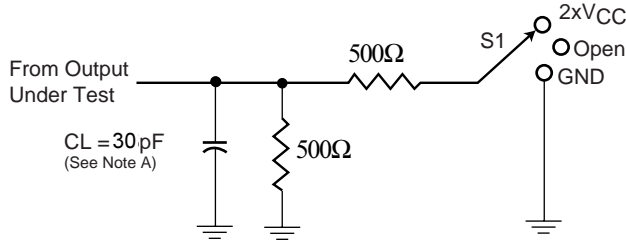
Figure3. Load Circuit and Voltage Waveforms

Notes:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.0\text{ns}$, $t_F \leq 2.0\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.
- t_{PLZ} and t_{PHZ} are the same as t_{dis}
- t_{PZL} and t_{PZH} are the same as t_{en}
- t_{PLH} and t_{PHL} are the same as t_{pd}

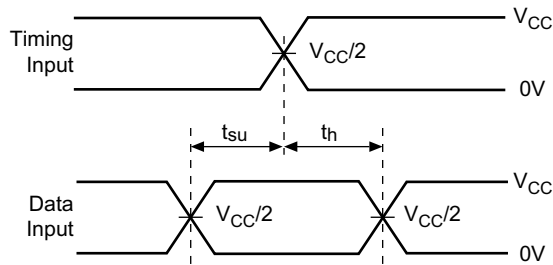
PARAMETER MEASUREMENT INFORMATION

$$V_{CC} = 3.3V \pm 0.3V$$

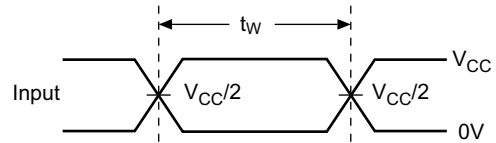


Load Circuit

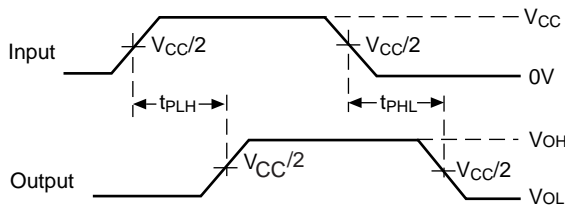
Test	S1
t _{pd} t _{PLZ} /t _{PZL} t _{PHZ} /t _{PZH}	Open 2 x V _{CC} GND



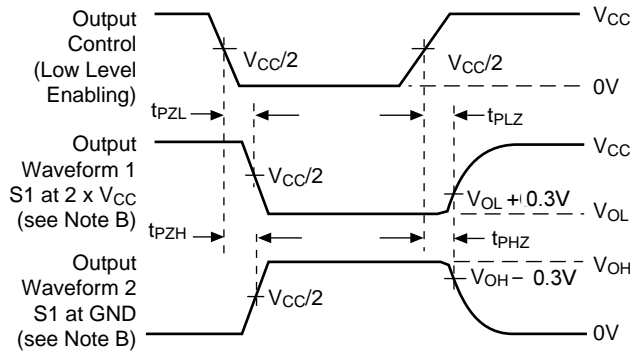
Voltage Waveforms
Setup and Hold Times



Voltage Waveforms
Pulse Duration



Voltage Waveforms
Propagation Delay Times



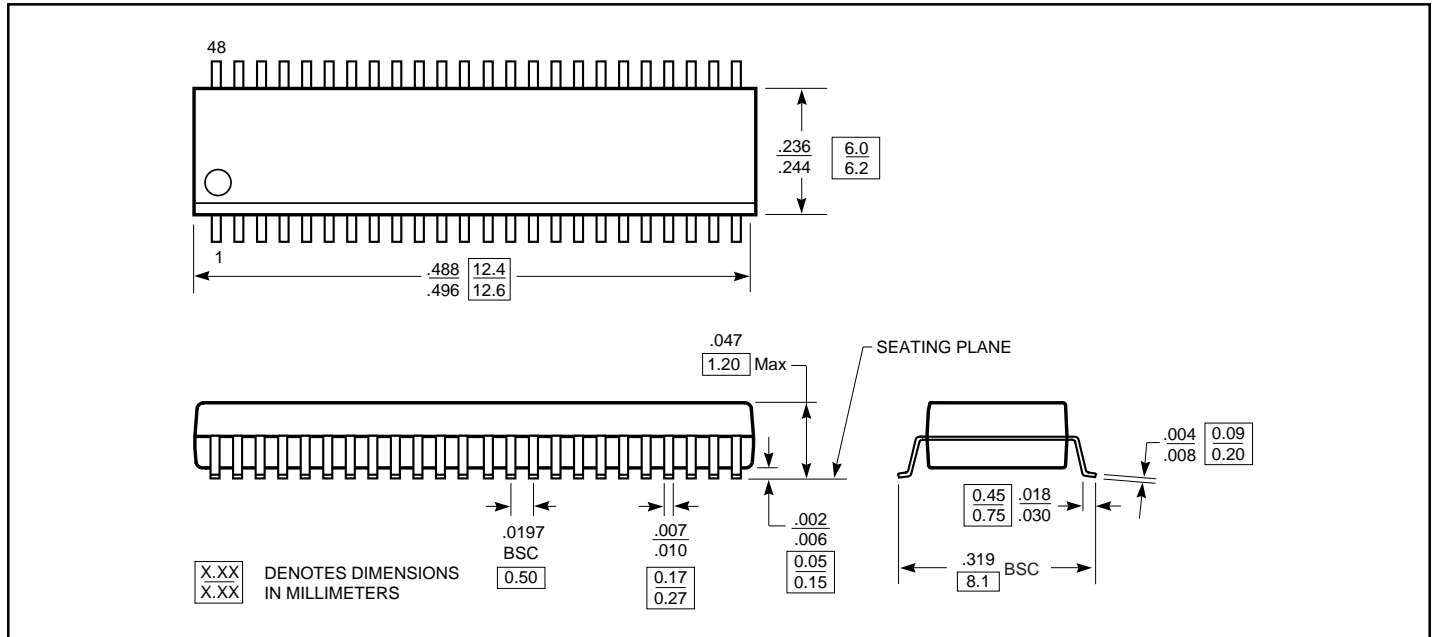
Voltage Waveforms
Enable and Disable Times

Figure 4. Load Circuit and Voltage Waveforms

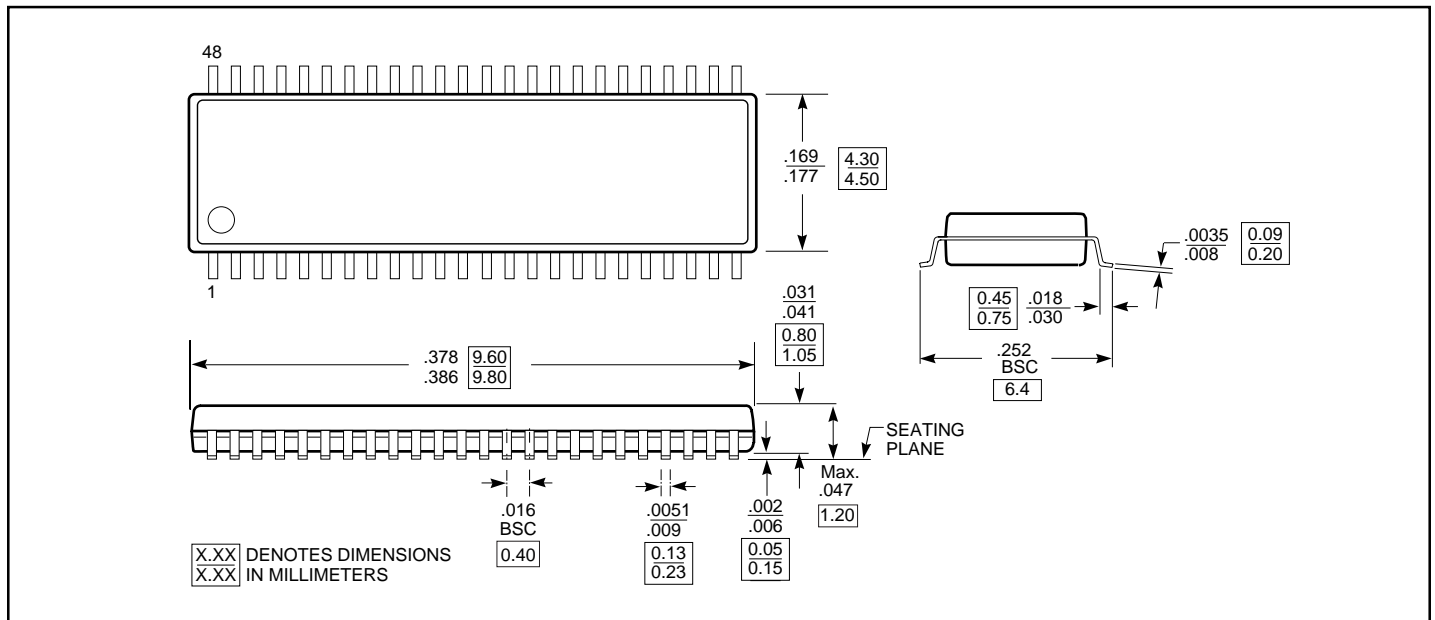
Notes:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50Ω, t_r ≤ 2.0ns, t_f ≤ 2.0ns.
- The outputs are measured one at a time with one transition per measurement.
- t_{PLZ} and t_{PHZ} are the same as t_{dis}
- t_{PZL} and t_{PZH} are the same as t_{en}
- t_{PLH} and t_{PHL} are the same as t_{pd}

48-pin TSSOP (A) Package



48-pin TVSOP (TSSOP) (K) Package



Ordering Information

Ordering Data	Description
PI74AVC+16244A	48-pin, 240-mil wide plastic TSSOP
PI74AVC+16244K	48-pin, 173-mil wide plastic TVSOP