

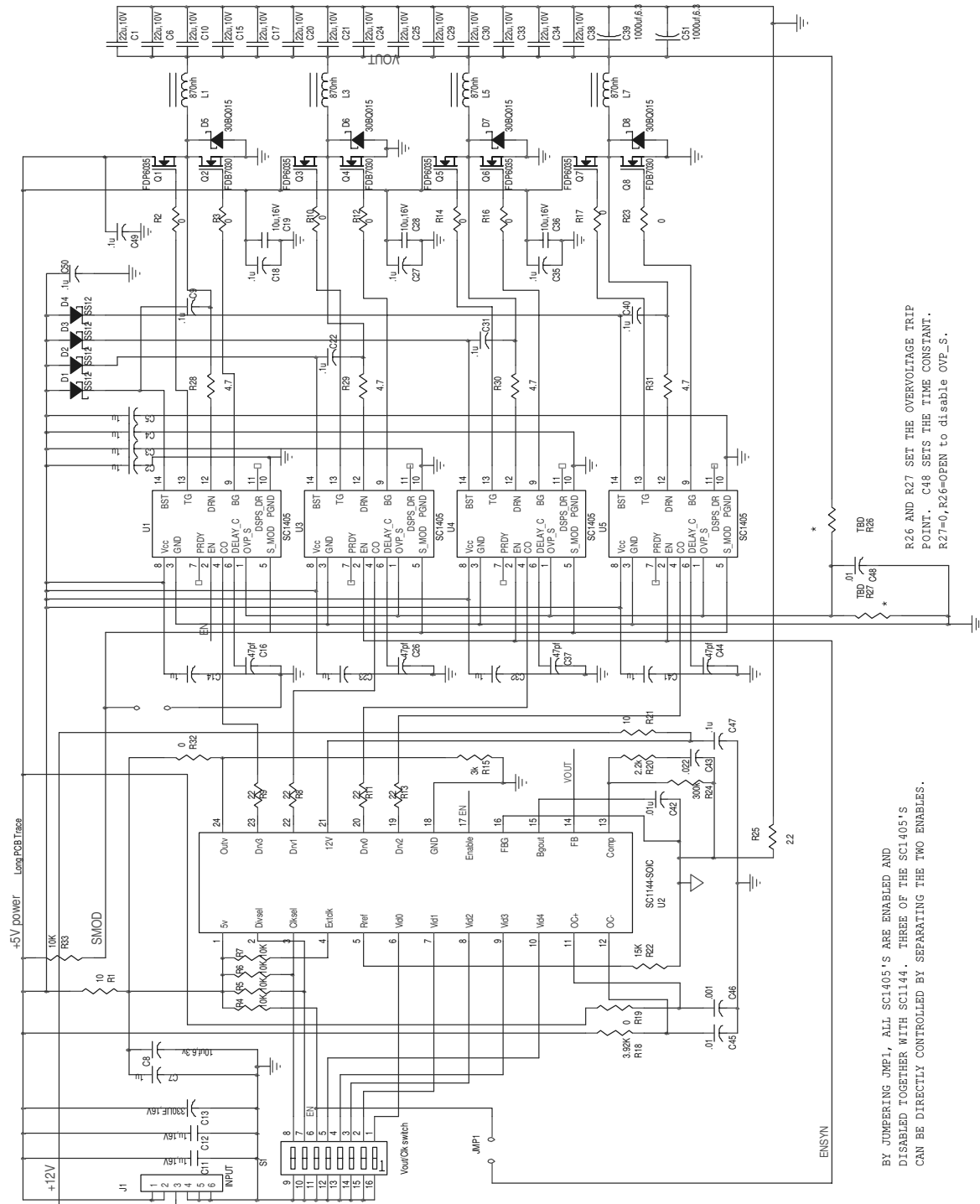


August 31, 2000

**APPLICATION EVALUATION BOARD SCHEMATIC**

SC1405/SC1144 Evaluation Board.

4-Phase synchronous, Freq.=1MHz



BY JUMPING JMP1, ALL SC1405'S ARE ENABLED AND  
DISABLED TOGETHER WITH SC1144. THREE OF THE SC1405'S  
CAN BE DIRECTLY CONTROLLED BY SEPARATING THE TWO ENABLES.

R26 AND R27 SET THE OVERVOLTAGE TRIP  
POINT. C48 SETS THE TIME CONSTANT.  
R27=0, R26=OPEN TO DISABLE OVP\_S.

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Figure 3