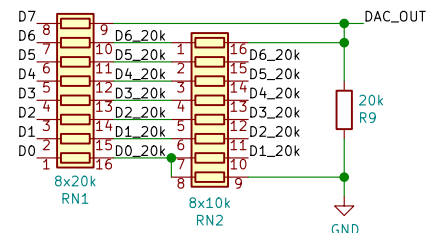


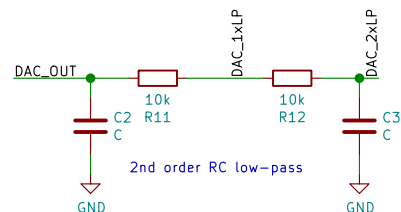
Note:
8x20k can be one of
- CTS 767163203G
- BI Technologies 8A203
- Bourns 4816P-1-203(LF)

Note:
8x10k can be one of
- CTS 767163103G
- BI Technologies 8A103
- Bourns 4816P-1-103(LF)

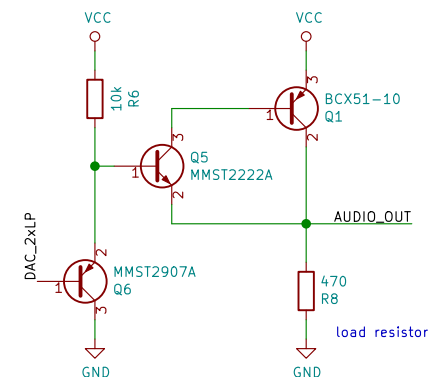
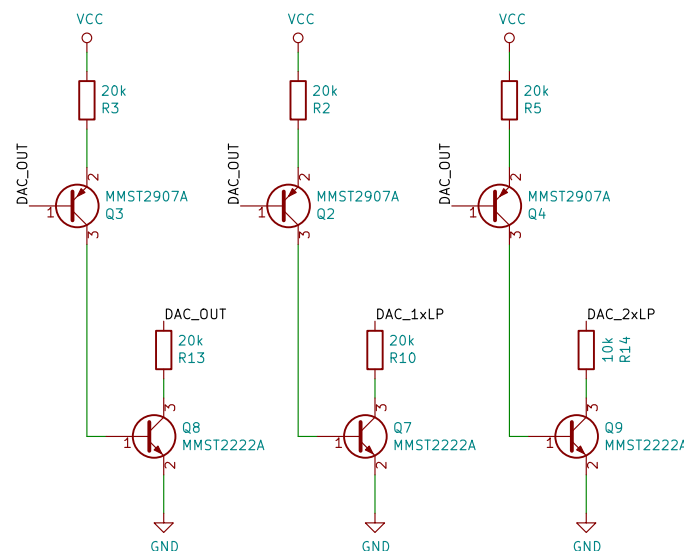


R-2R ladder with load resistor

Digital to analog conversion



2nd order RC low-pass



Note:
This schematic uses ordinary SC-59 packages, while the relevant components actually all use reversed SC-59 packages.
Pin numbers 1 and 2 have therefore been swapped. The pin positions remain identical.

Description of implicit volume control (one possibility)

- 0 (mute): All control lines, D0 and D1 forced to 0 → device powered from 0 pins
 - 1: All control lines and D0 forced to 0; D1 is 0.5 → device powered from 0.5 pins
 - 2: All control lines forced to 0; D0 and D1 are 0.5 → device powered from 1 pin
 - 3: All control lines 0, D0 forced to 1, D1 is 0.5 → device powered from 1.5 pins
 - 4: One control line 1, D0 and D1 are 0.5 → device powered from 2 pins
 - 5: Two control lines 1, D0 and D1 are 0.5 → device powered from 3 pins
 - 6: Three control lines 1, D0 and D1 are 0.5 → device powered from 4 pins
 - 7: Four control lines 1, D0 and D1 are 0.5 → device powered from 5 pins
- (0.5 means that the line is 1 50% of the time and 0 50% of the time)

Parallel Port Headphone DAC

Port-powered, with volume control
(SoundJr replica)

Benedikt Freisen

Sheet: /

File: parallel_port_headphone_dac.sch

Title: Parallel Port Headphone DAC

Size: A4 Date: 2019-04-13

KiCad E.D.A. kicad 5.1.0-5.1.0

Rev:

Id: 1/1