

Appendix II

Quick Connector and Jumper Reference

CPUFAN/SYSFAN: CPU/System cooling fan connector



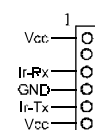
CN1: Chassis intrusion monitoring connector



CN2: WOL (Wake-on-LAN) Conn.

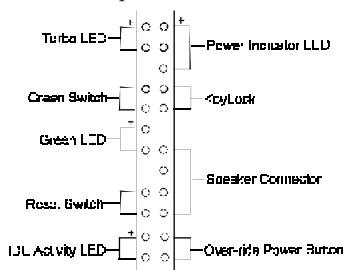


CN3: Infrared connector

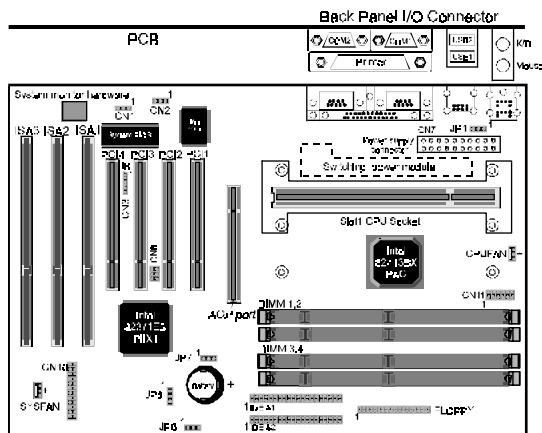


CN8: Creative's SB-Link connector
Link AWE64D with PCI Bus Mastering

CN10: Front panel connector set



CN11: FAN-II connector (optional)



SeePU (Jumper free CPU installation) setting table

CPU Type	CPU Speed		
	External Clock	Frequency Ratio	Internal Clock
Pentium II processor	66	3.5	233
		4	266
		4.5	300
		5	333
	100	3.5	350
		4	400
		4.5	450
		5	500

JP1: Power on by keyboard

- 1~2 short : Disabled
- 2~3 short : Enabled

JP6: Clear CMOS data jumper

- 1~2 short : Normal (default)
- 2~3 short : Clear CMOS data

JP7: 1~2 short (default)

JP8: Power failure recovery jumper

- 1~2 short: Disable, 2~3 short: Enable



Memo